

<b>GB Comparison Feature</b>	<b>GB660/CSA907A Tx and Rx</b>	<b>GB700 Tx and Rx</b>	<b>GB1400 Tx and Rx</b>
<i>Transmitter</i>			
Frequency range Internal Clock Source External Clock External Clock w/Burst Mode <sup>1</sup>	150 kHz to 700 MHz 150 kHz to 700 MHz 150 kHz to 700 MHz	150 kHz to 705 MHz 150 kHz to 705 MHz 150 kHz to 705 MHz	1 MHz to 1405 MHz 1 MHz to 1405 MHz 150 kHz to 1405 MHz
Freq Resolution CR/LF (Internal Clock)	1 kHz	1 kHz	1 kHz (was 10 kHz)
Clock /Data Output Amplitude	500mV to 2.0 V	500mV to 2.0 V	500mV to 2.0 V
Clock/Data Output Offset	-2.0V to +1.8 V	-2.0V to +1.8 V	-2.0V to +1.0 V -2.0V to +1.8 V with PECL opt.
Clock/Data Threshold Resolution	50 mV steps	50 mV steps	50 mV steps
Std. Programmable Memory	16 bits	128 Kbits	16 bits
Optional Memory	128 Kbits	none	1 Mbit
PRBS Patterns (2 <sup>n</sup> -1)	7,15,17,20,23	7,15,17,20,23	7,15,17,20,23
Burst Mode New Line (External Clk Only)	Standard Feature	Standard Feature	<b>Optional Feature</b>
<i>Receiver</i>			
Frequency range w/Burst Mode <sup>1</sup>	150 kHz to 705 MHz 150 kHz to 705 MHz	150 kHz to 705 MHz 150 kHz to 705 MHz	1 MHz to 1405 MHz 150 kHz to 1405 MHz
Burst Mode (Ext Clk Only)	Standard Feature	Standard Feature	<b>Optional Feature</b>
Clock/Data Input levels (max)	500 mV to 6.0 V p-p	500 mV to 6.0 V p-p	<b>500 mV to 2.0 V p-p</b>
Clock Input Threshold	-3.00 to +4.5 V	-3.00 to +4.5 V	<b>Fixed threshold levels</b>
Data Input Threshold	-3.00 to +4.5 V	-3.00 to +4.5 V	<b>-1.5V to 1.0 V</b>
Clock/Data Threshold Resolution	50 mV steps	50 mV steps	50 mV steps
Clock/Data Input Terminations	GND, AC, -2V, +3V	GND, AC, -2V, +3V	GND, AC, -2V
Single-Ended operation	Automatic selection	Automatic selection	<b>Requires external cable<sup>2</sup></b>
Clk/Data Delay Range / Resolution	±4 ns in 100 pS steps	±4 ns in 20 pS steps	±4 ns in 5 pS steps
<i>Note 1 Burst mode operation requires ECL levels and is DC coupled</i>			
<i>Note 2 Single-ended operation requires ext. cable connection from rear panel DATA THRESHOLD SMA</i>			